



01-07-02

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ko et al.

Serial No.: 09/585,682

Filed: June 1, 2000

For: SEMICONDUCTOR DEVICE HAVING A SUBSTRATE, AN UNDOPED SILICON OXIDE STRUCTURE, AND AN OVERLYING DOPED SILICON OXIDE STRUCTURE WITH A SIDE WALL TERMINATING AT THE UNDOPED SILICON OXIDE STRUCTURE

Examiner: C. Chu

Group Art Unit: 2815

Attorney Docket No.: 3526.2US (97-1136.2)

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. The listed documents are from co-pending application Serial No. 09/610,049, filed July 5, 2000. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other

possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

DOCUMENTS

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Issue Date</u>	<u>Inventor</u>
5,828,096	10/1998	Ohno et al.
5,831,899	11/1998	Wang et al.

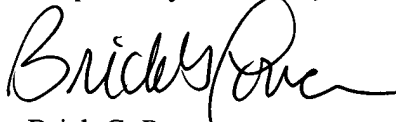
Other Documents

Wolf, Stanley, "Silicon Processing for the VLSI Era," cover pages and pages 194-195, Volume 2: Process Integration.

Applicants offer to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed after the mailing date of the first Office Action on the merits. I hereby certify that no item of information contained in the Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the statement.

Respectfully submitted,



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Date: January 3, 2002

BGP/hlg:djp

Enclosures: Form PTO-1449

Copy of documents cited